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Amendments to the Specification

Please insert the following paragraph at page 15, line 21:

Figure 15 is an illustration of the system of Fig. 2 with the adapting card replaced with a PCMCIA card.

Please amend the paragraph at page 20, lines 17-25 in the following manner:

The PC card detector 114 determines a type of a card connected or attached to (or inserted in) the PCMCIA card connector 113. When the inserted card is determined to be a PCMCIA card (Figure 15), the PC card detector 114 outputs a low-level card detection signal. When the inserted card is determined to be the non-PCMCIA card 123, the PC card detector 114 outputs a high-level card detection signal which is also referred to as a non-PCMCIA card detection signal.

Please amend the paragraph bridging pages 20 and 21 in the following manner:

When a PCMCIA card 125 is connected (Figure 15), the I/O circuit 130 enables a data line between the PCMCIA card connector 113 and the PCMCIA controller 116 and turns the analog switches 140 to OFF. Consequently, the PCMCIA card connector 113 and the PCMCIA controller 116 are connected via the data line. Data read from the PCMCIA card 125 is transmitted through the data lines L2, the I/O circuit 130 and the PC CardBus B104, and is output to the PCMCIA controller 116. Then, the data is further conveyed from the PCMCIA controller 116 through the PCI bus B103 to the chipset 112. Since the PCI bus B103 and the set of I/O circuit 130 communicate in a bidirectional manner, the data sent from the chipset 112 is transmitted to the PCMCIA card 125 via the PCI bus B103, the PCMCIA controller 116, the PCMCIA card bus B104, the I/O circuits 130 and the data lines L2.